

ABSTRACT OF THE DISCLOSURE

A tuner includes an input terminal, first to fourth band-pass filters, an automatic gain controller, first to third amplifiers, first and second mixers, first and second voltage controlled oscillators, first and second PLL ICs, a reference oscillator circuit, and an output terminal. The second PLL IC and the reference oscillator circuit are connected through a capacitor and an amplifier. An amplifier for reference oscillation, included in the first PLL IC is connected to the reference oscillator circuit through an inductor that defines an inductance device.

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